

BUS SEQUENCE OPERATION WITH AUTOMATIC LINKING FROM CURRENT I/O INFORMATION TO SUBSEQUENT I/O INFORMATION

Abstract of the Invention

In a computer system, a bus adapter processes bus operation information structures for performing bus operations by automatically starting processing each bus operation information structure after completing processing the previous bus operation information structure. A processor forms the bus operation information structures and sets control over each bus operation information structure to a sequencer for processing. When a next bus operation information structure is ready for processing after completing processing the previous bus operation information structure, the sequencer checks whether it has control over the next bus operation information structure, and if so, begins processing the next bus operation information structure without being instructed to do so by the processor.

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